Discipline: E. & TC Enggs.  Noof Days / per week class subject:  Week  Class Day  Ist Unit-1:Microprocessor (Architecture and Programming, 8085-8-bit) (15)  1st Unit-1:Microprocessor (Architecture and Programming, 8085-8-bit) (15)  1st Unit-1:Microprocessor (Architecture and Programming, 8085-8-bit) (15)  1.1 Introduction to Microprocessor and Microprocessor and Stronguish between them.  2			LESSON PLAN
Engs.  No of Days / per week dass Microcontroller  Week  Class Day  155  155  156  2nd  12 Concept of Address bus, Data bus, Control bus & System Bus  3rd  12 Concept of Address bus, Data bus, Control bus & System Bus  3rd  13 General Bus Structure Block diagram.  4th  14 Basic Architecture of 8085 (8 bit) Microprocessor  5th  14 Basic Architecture of 8085 (8 bit) Microprocessor  2nd  2nd  15 Signal Description (Pin diagram) of 8085 Microprocessor  3rd  15 Signal Description (Pin diagram) of 8085 Microprocessor  5th  16 Register Organizations, Distinguish between SPR & GPR  1st  1st Timing & Control Module  2nd  1r) Stack, Stack pointer & Stack top.  4th  1s Binerrupts-8085 interrupts, Masking of Interrupt(SIM,RIM)  5th  18 Interrupts-8085 interrupts, Masking of Interrupt(SIM,RIM)  1st 2.1 Addressing modes in instructions with suitable examples.  2nd  2.2 Addressing modes in instructions with suitable examples.  2nd  2.2 Addressing modes in instructions with suitable examples.  2nd  2.2 Addressing modes in instructions with suitable examples.  2nd  2.3 Simple Assembly Language Programming (15)  2.3 Simple Assembly Language Programming (18)  2.3 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction  4th  4th  2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction  4th  2.4.1 Logic Operations (AM), OR, Complement 1's & 2's) & Masking of bits and the control)  5th  2.4.2 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction  4th  2.4.3 Logic Operations (AM), OR, Complement 1's & 2's) & Masking of bits  2.4.4 Loggic Operations (AM), OR, Complement 1's & 2's) & Masking of bits  3rd  2.4.3 Code conversion, BCOAlfithmetic & 16 Bit data Operation, Block Transfer.  4th  2.4.3 Compare between two numbers  4th  3rd  3.4.4 Code conversion, BCOAlfithmetic	Discipline: E & TC	Semester: 4th	
Subject: Microprocessor & week class Microprocessor & week class dalotted: 5  Week  Class Day  1st  1st  1st  2nd  1st  1st  2nd  1st  1st  2nd  1st  1st  2nd  1st  2nd  1st  1st  1st  1st  1st  1st  1st  1s	· ·		and the same of th
Microcontroller         Microcontroller           Week         Class Day         Theory Topics           Interval Control of Microcontroller         1st         1st (Microcontroller)           Interval Control of Microcontroller         1st (Microcontroller)         1st (Microcontroller)           Interval Control of Microcontroller         1st (Microcontroller)         1st (Microcontroller)           Interval Controller         1st (Microcontroller)         1st (Microcontroller)           Interval Control         1st (Microcontroller)		No of Days /per	
No or Neters	Microprocessor &		
Ist    Dist   1.1 Introduction to Microprocessor   Architecture and Programming-8085-8-bit) (15)	Microcontroller	allotted: 5	No of weeks: 15
Ist    Unit-1:Microprocessor (Architecture and Programming-8085-8-bit) (15)	Week	Class Day	Theory Topics
1.1. Introduction to Microprocessor and Microcomputer & distinguish between them.  2 and 1.2 General Bus structure Block diagram.  4th 1.4 Basic Architecture of 80818 (Bit) Microprocessor  5th 1.4 Basic Architecture of 80818 (Bit) Microprocessor  1st 1.4 Basic Architecture of 80818 (Bit) Microprocessor  1st 1.4 Basic Architecture of 80818 (Bit) Microprocessor  2 nd 1.5 Signal Description (Pin diagram) of 8085 Microprocessor  2 nd 1.5 Signal Description (Pin diagram) of 8085 Microprocessor  4 th 1.5 Signal Description (Pin diagram) of 8085 Microprocessor  5 th 1.6 Register Organizations, Distinguish between SPR & GPR  1 st 1 Timing & Control Module  2 nd 1.7 Stack, Stack pointer & Stack top.  4 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 1.8 Interrupts - 8085 interrupts, Masking of Interrupt(SIM,RIM)  5 th 2.4 Addressing modes in instructions with suitable examples.  2 nd 2 Addressing modes in instructions with suitable examples.  2 nd 2 Addressing modes in instructions with suitable examples.  2 nd 2 Addressing modes in instructions with suitable examples.  3 nd 2 2 Addressing modes in instructions with suitable examples.  4 th 2 Addressing modes in instructions with suitable examples.  2 nd 3 Addressing modes in instructions with suitable examples.  2 nd 3 Addressing modes in instructions with suitable examples.  3 and 2 Addressing modes in instructions with suita			Unit-1:Microprocessor (Architecture and Programming-8085-8-bit) (15)
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4th 1.4 Basic Architecture of 8085 (8 bit) Microprocessor  5th 1.4 Basic Architecture of 8085 (8 bit) Microprocessor  1st 1.4 Basic Architecture of 8085 (8 bit) Microprocessor  1st 1.4 Basic Architecture of 8085 (8 bit) Microprocessor  1st 1.4 Basic Architecture of 8085 (8 bit) Microprocessor  2nd 1.5 Signal Description (Pin diagram) of 8085 Microprocessor  4th 1.5 Signal Description (Pin diagram) of 8085 Microprocessor  4th 1.5 Signal Description (Pin diagram) of 8085 Microprocessor  5th 1.6 Register Organizations, Distinguish between SPR & GPR  1st Timing & Control Module  2nd 1.7 Stack, Stack pointer & Stack top.  4th 1.8 Interrupts -8085 interrupts, Masking of Interrupt(SIM,RIM)  5th 1.8 Interrupts -8085 interrupts, Masking of Interrupt(SIM,RIM)  1st 1.1 Addressing data & Differentiate between one-byte, two-byte & 8three-byte instructions with examples.  2.1 Addressing modes in instructions with suitable examples.  2.2 Addressing modes in instructions with suitable examples.  2.3 Instruction Sect of 8085 (Data Transfer, Arithmetic, Logical, Branching, Stack&1/O, Machine Control)  2.3 Instruction Sect of 8085 (Data Transfer, Arithmetic, Logical, Branching, Stack&1/O, Machine Control)  1st 2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction and Control (Control)  2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction and Control (Control)  2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction and Control (Control)  3rd 2.4.2 Logic Operations (AND, OR, Complement 15 & 25) & Masking of bits to 2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register)  5th 2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register)  5th 2.4.5 Stack & Subroutine programes.  2nd 2.4.6 Code conversion, BCO Arithmetic & 16 Bit data Operation, Block Transfer.  4th 2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register)  5th 2.4.6 Code conversion, BCO Arithmetic		2nd	1.2 Concept of Address bus, Data bus, Control bus & System Bus
Sth		3rd	1.3 General Bus structure Block diagram.
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2nd 1.5 Signal Description (Pin diagram) of 8085 Microprocessor  3rd 1.5 Signal Description (Pin diagram) of 8085 Microprocessor  4th 1.5 Signal Description (Pin diagram) of 8085 Microprocessor  5th 1.6 Register Organizations, Distinguish between SPR & GPR  1st Timing & Control Module  2nd 1.7 Stack, Stack pointer & Stack top.  3rd 1.7 Stack, Stack pointer & Stack top.  4th 1.8 Interrupts-8085 Interrupts, Masking of Interrupt(SIM,RIM)  5th 1.8 Interrupts-8085 Interrupts, Masking of Interrupt(SIM,RIM)  Unit-2: Instruction Set and Assembly Language Programming (15)  1st 2.1 Addressing data & Differentiate between one-byte, two-byte & three-byte instructions with examples.  2nd 2.2 Addressing modes in instructions with suitable examples.  2nd 2.2 Addressing modes in instructions with suitable examples.  4th 3rd 2.2 Addressing modes in instructions with suitable examples.  4th Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction Machine Control)  5th 2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction and 2.4.2 Logic Operations (AND, OR, Complement 1's & 2's) & Masking of bits  4th 2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register)  5th 2.4.4 Lopping, Counting & Indexing (Call/JMP etc).  1st 2.4.5 Stack & Subroutine programms  4th 2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register)  5th 2.4.4 Lopping, Counting & Indexing (Call/JMP etc).  1st 2.4.5 Stack & Subroutine programms  3rd 2.4.7 Compare between two numbers  4th 2.4.8 Array Handling (Largest number & smallest number in the array)  5th 2.5 Memory & I/O Addressing,  Unit-3: TIMING DIAGRAMS. (8)  3.1 Define oppode, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram for memory read, memory write, I/O read, I/O write machine cycle.  4th 3.2 Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle.		5th	1.4 Basic Architecture of 8085 (8 bit) Microprocessor
3rd 1.5 Signal Description (Pin diagram) of 8085 Microprocessor 4th 1.5 Signal Description (Pin diagram) of 8085 Microprocessor 5th 1.6 Register Organizations, Distinguish between SPR & GPR  1st Timing & Control Module 2nd 1.7 Stack, Stack pointer & Stack top. 3rd 3rd 1.7 Stack, Stack pointer & Stack top. 4th 1.8 Interrupts: 8085 Interrupts, Masking of Interrupt(SIM,RIM) 5th 1.8 Interrupts: 8085 Interrupts, Masking of Interrupt(SIM,RIM) 5th 1.8 Interrupts: 8085 Interrupts, Masking of Interrupt(SIM,RIM)  Unit: 2: Instruction Set and Assembly Language Programming (15) 1st 2.1 Addressing data & Differentiate between one-byte, two-byte & three-byte instructions with examples. 2nd 2.2 Addressing modes in instructions with suitable examples. 2.3 Instruction Set of 8085 (Data Transfer, Arithmetic, Logical, Branching, Stack& I/O, Machine Control) 5th 2.3 Instruction Set of 8085 (Data Transfer, Arithmetic, Logical, Branching, Stack& I/O, Machine Control)  1st 2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction 3rd 2.4.2 Logic Operations (AND, OR, Complement 1's & 2's) & Masking of bits 4th 2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register) 5th 2.4.4 Lopping, Counting & Indexing (Call/JMP etc). 1st 2.4.5 Stack & Subroutine programes. 2nd 2.4.6 Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer. 3rd 2.4.7 Compare between two numbers 4th 2.4.8 Array Handling (Largest number & smallest number in the array) 5th 2.5 Memory & I/O Addressing.  Unit-3: TiMING DIAGRAMS. (8) 1st 3.1 Define opcode, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram. 2nd 3.2 Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle.		1st	1.4 Basic Architecture of 8085 (8 bit) Microprocessor
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3rd	3rd	1st	Timing & Control Module
4th 1.8 Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM) 5th 1.8 Interrupts:-8085 Interrupts, Masking of Interrupt(SIM,RIM) Unit-2: Instruction Set and Assembly Language Programming (15) 1st 2.1 Addressing data & Differentiate between one-byte, two-byte &three-byte instructions with examples. 2nd 2.2 Addressing modes in instructions with suitable examples. 4th 3rd 2.2 Addressing modes in instructions with suitable examples. 2.3 Instruction Set of 8085 (Data Transfer, Arithmetic, Logical, Branching, Stack& I/O, Machine Control) 5th 2.3 Instruction Set of 8085 (Data Transfer, Arithmetic, Logical, Branching, Stack& I/O, Machine Control) 1st 2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction 2nd 2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction 3rd 2.4.2 Logic Operations (AND, OR, Complement 1's & 2's) & Masking of bits 4th 2.4.3 Counters & Time delay (Single Register, Register Pair, Morethan Two Register) 5th 2.4.4 Looping, Counting & Indexing (Call/IMP etc). 1st 2.4.5 Stack & Subroutine programes. 2nd 2.4.6 Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer. 4th 2.4.8 Array Handling (Largest number & smallest number in the array) 5th 2.5 Memory & I/O Addressing, Unit-3: TIMING DIAGRAMS. (8) 1st 3.1 Define opcode, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram. 3.1 Define opcode, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram. 3.1 Define opcode, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram for memory read, memory write, I/O read, I/O write machine cycle. 4th 3.2 Draw timing diagram for memory read, memory write, E.		2nd	1.7 Stack, Stack pointer &Stack top.
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Unit-2: Instruction Set and Assembly Language Programming (15)  1st 2.1 Addressing data & Differentiate between one-byte, two-byte & three-byte instructions with examples.  2nd 2.2 Addressing modes in instructions with suitable examples.  4th 3rd 2.2 Addressing modes in instructions with suitable examples.  4th Machine Control)  5th Machine Control)  1st 2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction  2nd 2.4.2 Imple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction  3rd 2.4.2 Logic Operations (AND, OR, Complement 1's & 2's) & Masking of bits  4th 2.4.3 Counters & Time delay (Single Register, Register Pair, More than Two Register)  5th 2.4.4 Looping, Counting & Indexing (Call /JMP etc).  1st 2.4.5 Stack & Subroutine programes.  2nd 2.4.6 Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer.  4th 2.4.8 Array Handling (Largest number & smallest number in the array)  5th 2.5 Memory & I/O Addressing,  Unit-3: TiMING DIAGRAMS. (8)  1st 3.1 Define opcode, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram.  3rd 3.2 Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle.  4th 3.2 Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle.		4th	
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4th 3rd 2.2 Addressing modes in instructions with suitable examples.  2.3 Instruction Set of 8085 (Data Transfer, Arithmetic, Logical, Branching, Stack& I/O, Machine Control)  5th 2.3 Instruction Set of 8085 (Data Transfer, Arithmetic, Logical, Branching, Stack& I/O, Machine Control)  1st 2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction  2nd 2.4 Simple Assembly Language Programming of 8085 2.4.1 Simple Addition & Subtraction  3rd 2.4.2 Logic Operations (AND, OR, Complement 1's & 2's) & Masking of bits  4th 2.4.3 Counters & Time delay (Single Register, Register Pair, Morethan Two Register)  5th 2.4.4 Looping, Counting & Indexing (Call/JMP etc).  1st 2.4.5 Stack & Subroutine programes.  2nd 2.4.6 Code conversion, BCD Arithmetic & 16 Bit data Operation, Block Transfer.  6th 3rd 2.4.7 Compare between two numbers  4th 2.4.8 Array Handling (Largest number & smallest number in the array)  5th 2.5 Memory & I/O Addressing,  Unit-3: TIMING DIAGRAMS. (8)  1st 3.1 Define opcode, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram.  2nd 3.1 Define opcode, operand, T-State, Fetch cycle, Machine Cycle, Instruction cycle & discuss the concept of timing diagram.  3rd 3.2 Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle.  4th 3.2 Draw timing diagram for memory read, memory write,  5th 3.2 Draw timing diagram for memory yerite,		2nd	·
2.3 Instruction Set of 8085(Data Transfer, Arithmetic, Logical, Branching, Stack& I/O, Machine Control)  5th	4th		-
Machine Control    Sth			
Sth   Machine Control		4th	·
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5th 3.2 Draw timing diagram for I/O read, I/O write machine cycle.		3rd	3.2 Draw timing diagram for memory read, memory write, I/O read, I/O write machine cycle.
		4th	3.2 Draw timing diagram for memory read, memory write,
1st 3.3 Draw a neat sketch for the timing diagram for 8085 instruction MOV instruction).		5th	3.2 Draw timing diagram for I/O read, I/O write machine cycle.
		1st	3.3 Draw a neat sketch for the timing diagram for 8085 instruction MOV instruction).

8th	2nd	3.3 Draw a neat sketch for the timing diagram for 8085 instruction MVI instruction).
	3rd	3.3 Drawa neat sketch for the timing diagram for 8085 instruction LDA instruction.
	4th	Unit-4 Microprocessor Based System Development Aids (10) 4.1 Concept of interfacing
	5th	4.2 Define Mapping & Data transfer mechanisms - Memory mapping & I/O Mapping
9th	1st	4.3 Concept of Memory Interfacing:-Interfacing EPROM & RAM Memories
	2nd	4.4 Concept of Address decoding for I/O devices
	3rd	4.5 Programmable Peripheral Interface: 8255
	4th	4.5 Programmable Peripheral Interface: 8255
	5th	4.6 ADC & DAC with Interfacing.
10th	1st	4.6 ADC & DAC with Interfacing.
	2nd	4.7 Interfacing Seven Segment Displays
	3rd	4.8 Generate square waves on all lines of 8255
	4th	4.9 Design Interface a traffic light control system using 8255.
	5th	4.9 Design Interface a traffic light control system using 8255.
11th	1st	4.10 Design interface for stepper motor control using 8255.
	2nd	4.11 Basic concept of other Interfacing DMA controller, USART
	2110	Unit-5 Microprocessor (Architecture and Programming-8086-16 bit) (12)
	3rd	5.1 Register Organisation of 8086
	4th	5.2 Internal architecture of 8086
	5th	5.2 Internal architecture of 8086
	1st	5.3 Signal Descriptionof 8086
	2nd	5.3 Signal Description of 8086
40.1	3rd	5.4 General Bus Operation& Physical Memory Organisation
12th	4th	5.5 MinimumMode&Timings, 5.6 Maximum Mode&Timings,
		5.7 Interrupts and Interrupt Service Routines, Interrupt Cycle, Non-Maskable Interrupt,
	5th	Maskable Interrupt
	1-4	5.8 8086 Instruction Set & Programming: Addressing Modes, Instruction Set, Assembler
13th	1st	Directives and Operators,
	2nd	5.9 Simple Assembly language programmingusing 8086 instructions.
	3rd	Unit-6 Microcontroller (Architecture and Programming-8 bit) (15)
		6.1 Distinguish between Microprocessor & Microcontroller
	4th	6.2 8 bit & 16 bit microcontroller 6.3 CISC & RISC processor
	5th	6.4 Architectureof8051Microcontroller
14th	1st	6.4 Architectureof8051Microcontroller
	2nd	6.5 Signal Descriptionof8051Microcontrollers
	3rd	6.6 Memory Organisation-RAM structure, SFR
	4th	6.7 Registers, timers, interrupts of 8051 Microcontrollers
	5th	6.8 Addressing Modes of 8051
15th		6.9 Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions , JUMP,
	1st	LOOP, CALL Instructions, I/O Port Programming
	2 ~ 4	6.9 Simple 8051 Assembly Language Programming Arithmetic & Logic Instructions , JUMP,
	2nd	LOOP, CALL Instructions, I/O Port Programming
	3rd	6.10 Interrupts, Timer & Counters
	4th	6.11 Serial Communication
	5th	6.12 Microcontroller Interrupts and Interfacing to 8255